

TITLE: CHECKSUM WRITING METHOD AND
CHECKSUM CHECKING APPARATUS
INVENTORS: Jin-hun KIM
SERIAL NO.: To Be Assigned
DOCKET NO.: 1793.1158

FIG. 1

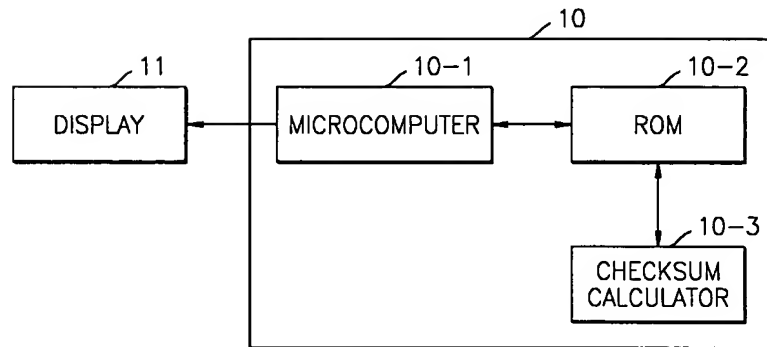


FIG. 2 (PRIOR ART)

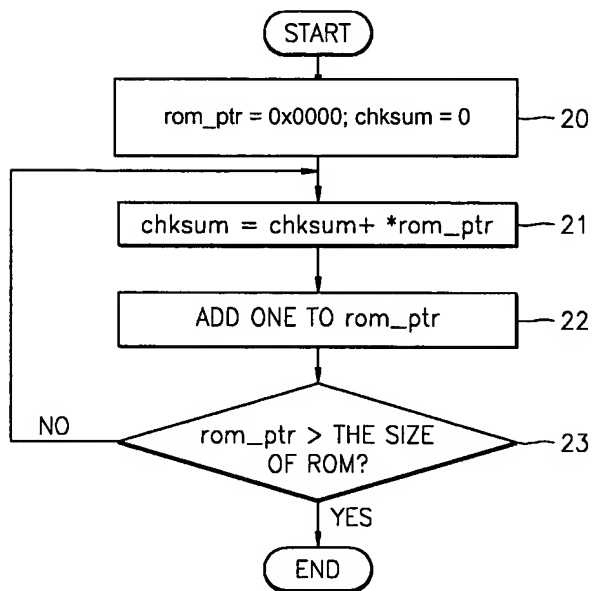


FIG. 3

Diagram illustrating memory addresses and their corresponding values:

30		\vdots	31
0003ffa0h: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF			
0003ffb0h: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF			
0003ffc0h: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF			
0003ffd0h: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF			
0003ffe0h: FF FF FF FF FF FF FF FF FF FF FF FF FF FF FF			
0003fff0h: FF FF FF FF FF FF FF FF D3 FD FF FF			
			(FF) (FF) (FF)
			C A B

FIG. 4

